



Application Number  **SEARCH**

IDS Flag Clearance for Application

**IDS  
Information**

Content	Mailroom Date	Entry Number	IDS Review	Reviewer
M844	05-04-2004	13	<input checked="" type="checkbox"/>	01-25-2005 13:45:24 jminor

**UPDATE**

LAST - [Additional LAST TextSearch.wsp:1]

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Drafts BRS: BRS: BRS:  
Pending Active L2: (3369) (digitalS2 near3 oscillator) and memory and c  
L3: (258) 2 and (set near2 point)  
L4: (148) 3 and (IC or (integrated adj circuit))  
L5: (17) 2 and ballast  
L7: (6) 6 and (IC or (integrated adj circuit))  
L6: (7) 5 and (set near2 point)  
Failed Saved Favorites Tagged (0) UDC Queue Trash

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DBs: US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM\_TDB  
Default operator: OR Plurals Highlight all hit terms initially

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Er	Error
1	BRS	L2	3369	(digitalS2 near3 oscillator) and memory and controlS4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/08 12:30			
2	BRS	L3	258	2 and (set near2 point)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/08 13:07			
3	BRS	L4	148	3 and (IC or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/08 12:30			
4	BRS	L5	17	2 and ballast	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/08 12:30			
5	BRS	L7	6	6 and (IC or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/08 12:30			
6	BRS	L6	7	5 and (set near2 point)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/08 13:12			

LAST - [Updated\_LAST\_Text.pts.wsp]

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Active

- L6: (618) (IC or (integrated adj circuit)) and (storage or memory) and (processor or microprocessor) and (digital near2 oscillator)
- L7: (47) 6 and (power adj switch)
- L8: (47) 7 and frequency
- L9: (954) (IC or (integrated adj circuit)) and (storage or memory) and (processor or microprocessor) and (digital near2 oscillator)
- L10: (122) 9 and (minimS2 near2 frequency)
- L12: (2) 11 and (power adj switch)
- L11: (79) 10 and (lamp or load)
- L28: (2426) (IC or (integrated adj circuit)) and (storage or memory) and (processor or microprocessor) and (digital near2 oscillator)
- L29: (246) 28 and (minimS2 near2 frequency)
- L30: (138) 29 and (lamp or load)
- L31: (2085) (IC or (integrated adj circuit)) and (storage or memory) and (processor or microprocessor) and (digital near2 oscillator)
- L32: (227) 31 and (minimS2 near2 frequency)
- L33: (133) 32 and (lamp or load)

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	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Er	Error
1	BRS	L6	618	(IC or (integrated adj circuit)) and (storage or memory) and (processor or microprocessor) and (digital near2 oscillator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/05 15:47			
2	BRS	L7	47	6 and (power adj switch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/05 15:49			
3	BRS	L8	47	7 and frequency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/05 15:48			
4	BRS	L9	954	(IC or (integrated adj circuit)) and (storage or memory) and controlS4 and (digital near2 oscillator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/05 16:32			
5	BRS	L10	122	9 and (minimS2 near2 frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/05 16:33			
6	BRS	L12	2	11 and (power adj switch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/05 15:49			
7	BRS	L11	79	10 and (lamp or load)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/05 16:33			
8	BRS	L28	2426	(IC or (integrated adj circuit)) and (storage or memory) and controlS4 and (digital near2 oscillator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/05 16:39			
9	BRS	L29	246	28 and (minimS2 near2 frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/05 16:40			
10	BRS	L30	138	29 and (lamp or load)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/05 16:40			
11	BRS	L31	2085	(IC or (integrated adj circuit)) and (storage or memory) and controlS4 and (digital near2 oscillator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/05 16:39			
12	BRS	L32	227	31 and (minimS2 near2 frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/05 16:40			
13	BRS	L33	133	32 and (lamp or load)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2006/01/05 16:44			

Hits Details HTML

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NUM SCR